

This is a quick walkthrough to use the ZeitControl IDE for building the HOTPC.BAS image from source. An Image file is included in the ootp distribution. This procedure is only necessary to modify the source code.

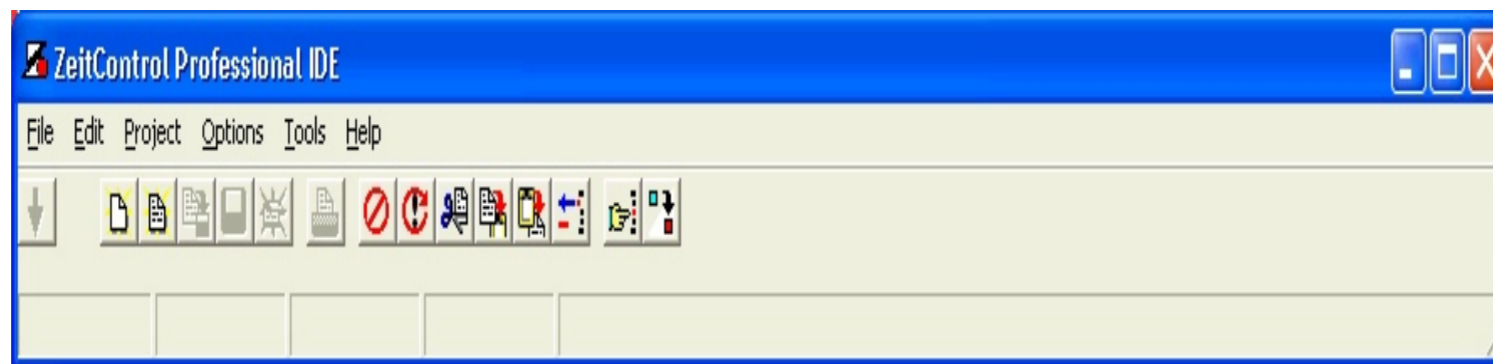
The IDE compiles HOTPC.BAS to compact PCODE and stores the result in HOTPC.IMG which is then downloaded to a ZC3.9 SmartCard using the IDE or Unix/Linux bclload utility.

The IDE provides an editor, source level debugger, Smart Card and Terminal emulation, and numerous examples.

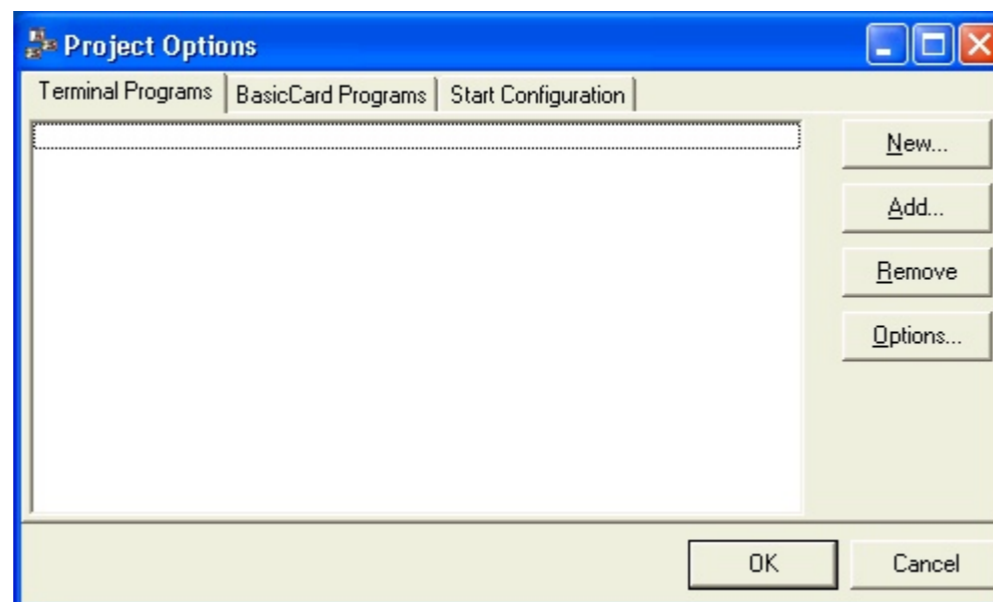
The ZeitControl BasicCard IDE is available as a free download from <http://www.basiccard.com>

Create a folder ootp-1.03 with HOTPC.BAS and HOTP.DEF from ootp-release-1.03.tar.gz

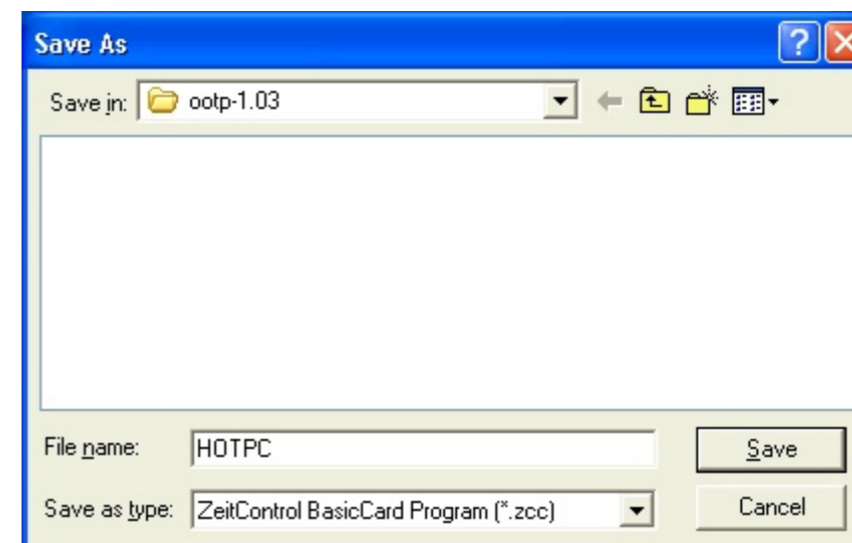
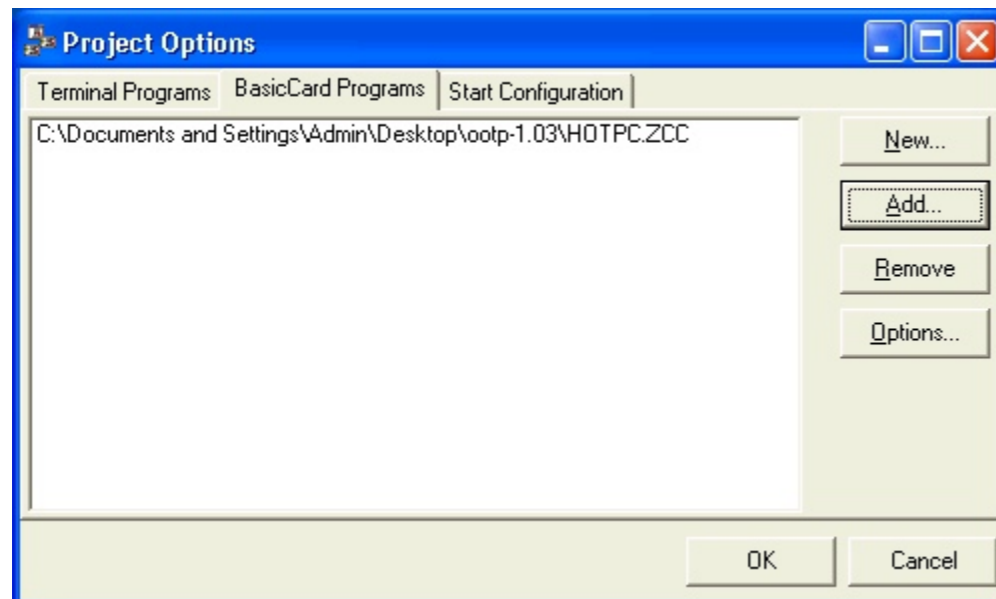
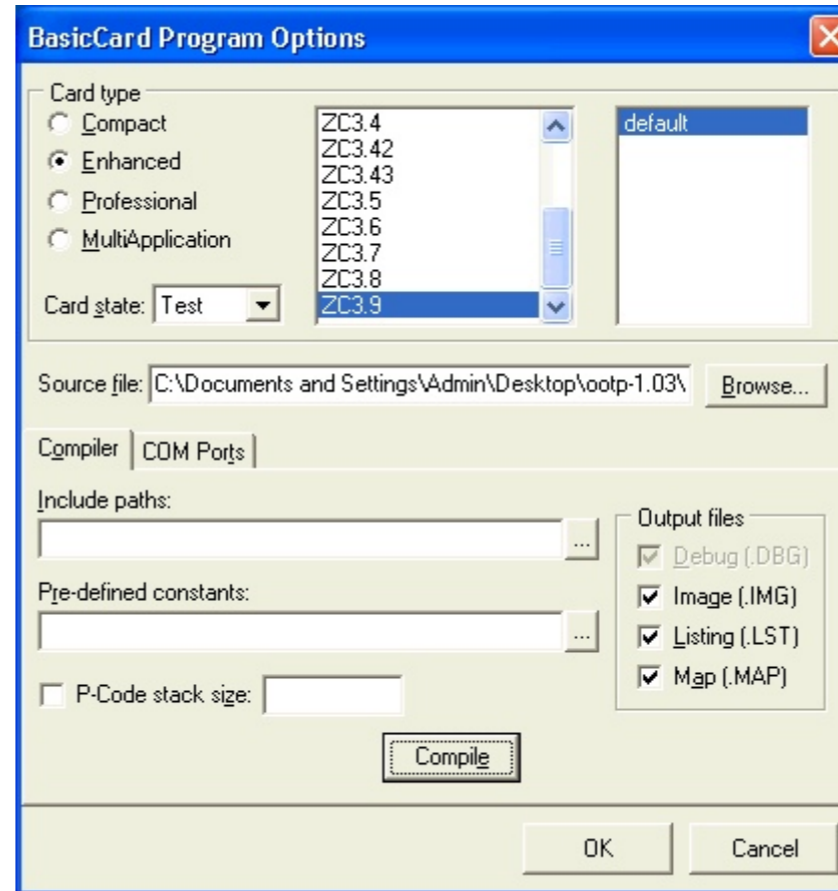
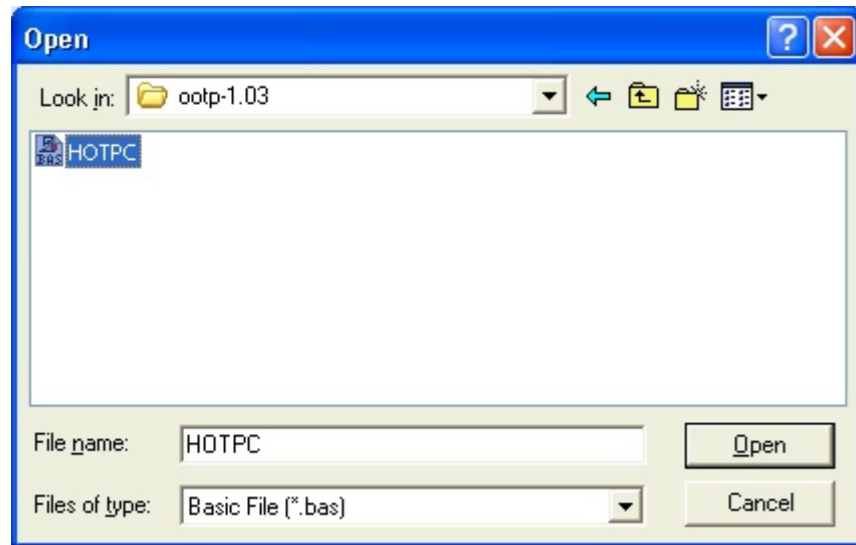
Start ZeitControl IDE



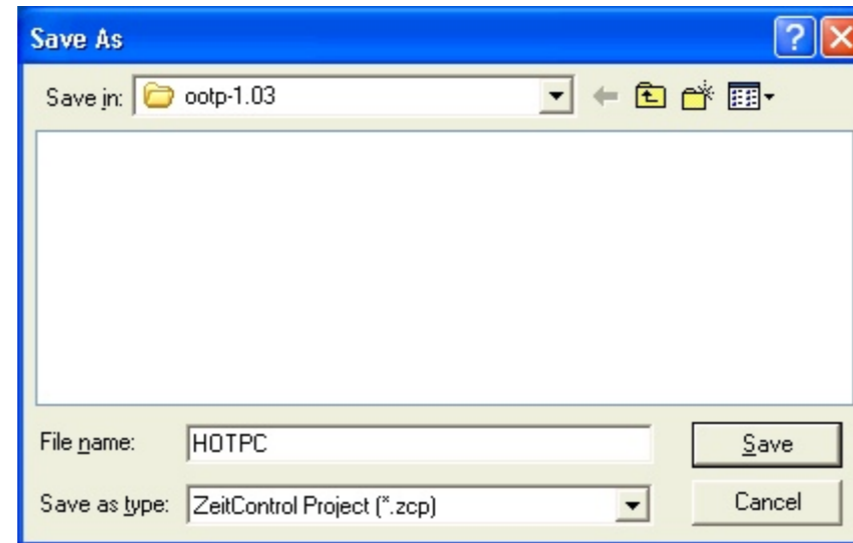
Select Project/Options. Click on BasicCard Programs



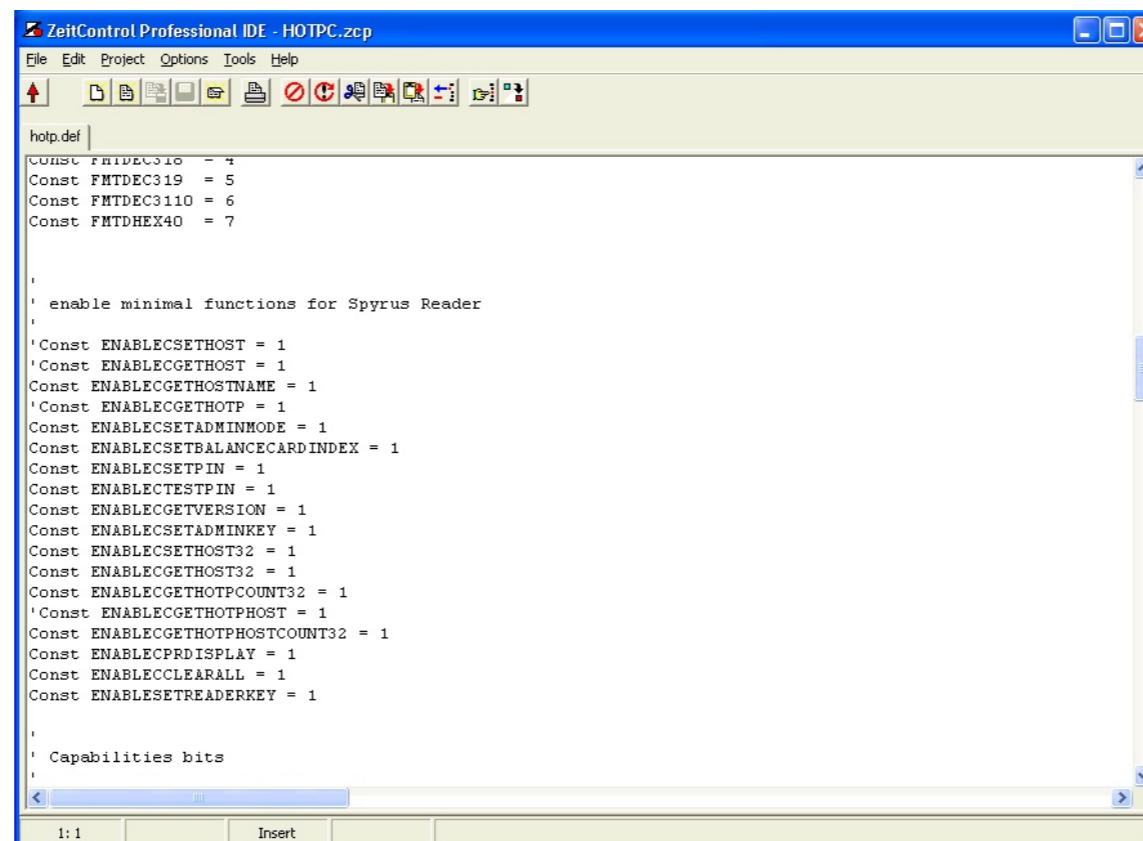
Click Add. Add HOTPC.BAS to the project as a BasicCard program. The card type is ZC3.9. Select the Image file option. Save as HOTPC.ZCC



Click Compile. Save project as HOTPC.ZCP



At this point you may change the source code and re-compile, for example remove the `ENABLEPRDISPLAY` to reduce code size of the Balance Reader is not used.



Use the IDE to burn the HOTPC.IMG file to the SC
This may also be done from Unix/Linux with the supplied
bclload utility. Select Tools/Download to card, then select
the image and SC port. Click Download to start.

